

This listing of claims will replace all prior versions, and listings, of claims in the application

Listing of Claims

1. (currently amended) A large area display, comprising:
a pixel layer having a repeating pattern of sub-displays formed on a continuous pixel layer sheet,
the subdisplays including display elements;
a plurality of drivers, each driver in communication with a corresponding one of the sub-displays
and configured for driving the display elements in the sub-display~~pixel layer~~; and
a connection layer in communication with the drivers having a continuous sheet with conductive
traces for distributing power and data to the drivers wherein the pixel layer, the
connection layer and the drivers are laminated together to form the large area display.
2. (original) The large area display according to claim 1, wherein the drivers are
laminated between the pixel layer and the connection layer.
3. (original) The large area display according to claim 1, wherein the display
elements comprises at least one of liquid crystal display (LCD), light emitting diode (LED),
organic LED (OLED), polymer light emitting device (PLED), electroluminescent (EL),
electrophoretic display, electrochromic display, electrowetting, gas plasma and fiber plasma.
4. (original) The large area display according to claim 1, wherein the pixel layer
comprises an active matrix display.
5. (original) The large area display according to claim 1, wherein the pixel layer
comprises a passive matrix display.
6. (original) The large area display according to claim 1, wherein the pixel layer
comprises at least one transistor per pixel.
7. (original) The large area display according to claim 6, wherein each of the at least
one transistors comprises a thin film transistor (TFT).

8. (original) The large area display according to claim 1, wherein the connection layer comprises a first conductive layer for providing power and ground connections to driver electronics and a second conductive layer for providing serial data connectivity to an input/output (I/O) connector.

9. (original) The large area display according to claim 1, wherein the connection layer comprises low voltage differential signaling (LVDS) logic for data transmission.

10. (original) The large area display according to claim 1, wherein the drivers comprise complementary metal on semiconductor (CMOS) circuitry on silicon or glass substrates.

11. (original) The large area display according to claim 1, wherein the drivers comprise complementary metal on semiconductor (CMOS) circuitry on plastic substrates.

12. (original) The large area display according to claim 1, wherein the drivers further comprise:
serial data input for receiving display data; and
serial data output for sensing and testing.

13. (original) The large area display according to claim 1, further comprising an input/output (I/O) connector in communication with the connection layer configured for external communication.

14-44 (canceled)